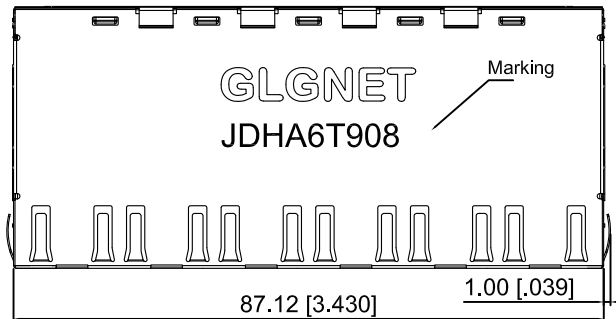


Mechanical

NO.	ECN NO.	DESCRIPTION	REV.	DRAW	DATE



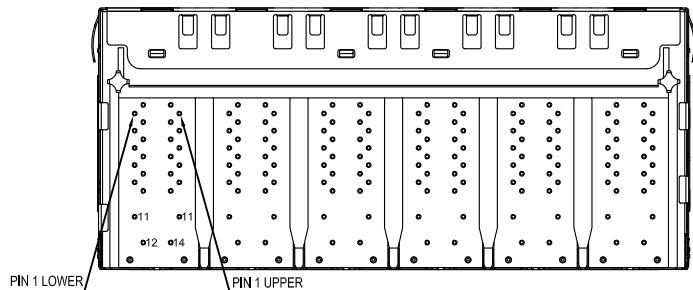
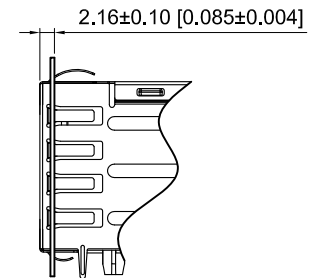
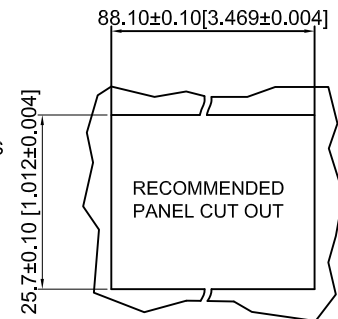
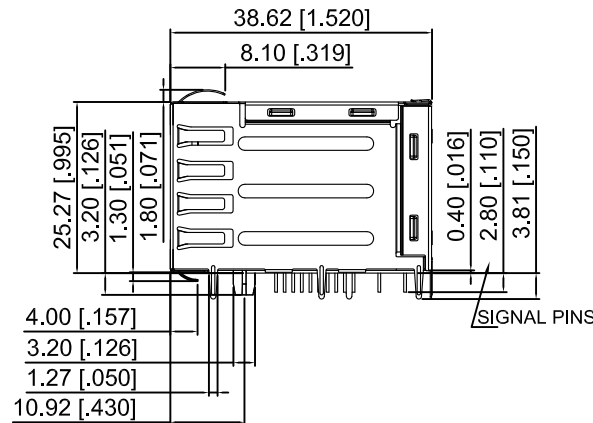
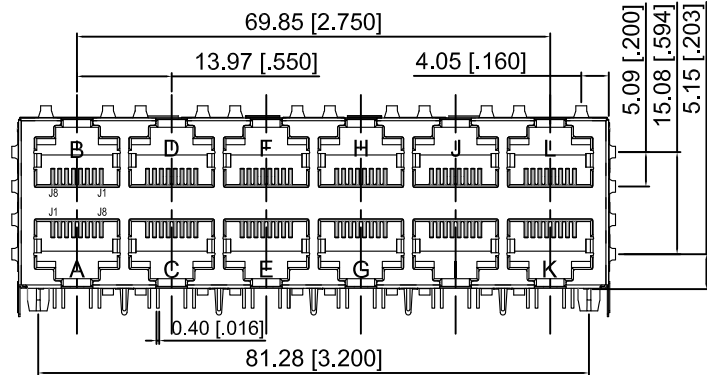
GoldPlating	Option
1.8μ"	<input type="checkbox"/>
3μ"	<input type="checkbox"/>
6μ"	<input checked="" type="checkbox"/>
15μ"	<input type="checkbox"/>
30μ"	<input type="checkbox"/>
50μ"	<input type="checkbox"/>

MATERIALS

- HOUSING: GLASS FILLED PA46 (BLACK) UL94V-0 (ROHS)
- CONTACT: PHOSPHOR BRONZE, 50-100 MICROINCHES MIN. OVERALL DUCTILE NICKEL UNDERPLATE WITH SELECTED 6U MICROINCHES GOLD AT MATING INTERFACE
- SHIELD: 0.2mm THICKNESS WITH BRASS.
- JACK CAVITY CONFORMS TO FCC RULES AND REGULATIONS PART 68 SUBPART F
- THE PART IS RECOMMENDED FOR WAVE SOLDERING PROCESS PEAK SOLDERING TEMPERATURE IS 260°C MAX, 10 SECS MAX.
- OPERATING TEMPERATURE: 0°C TO +70°C. STORAGE TEMPERATURE: -40°C TO +85°C.

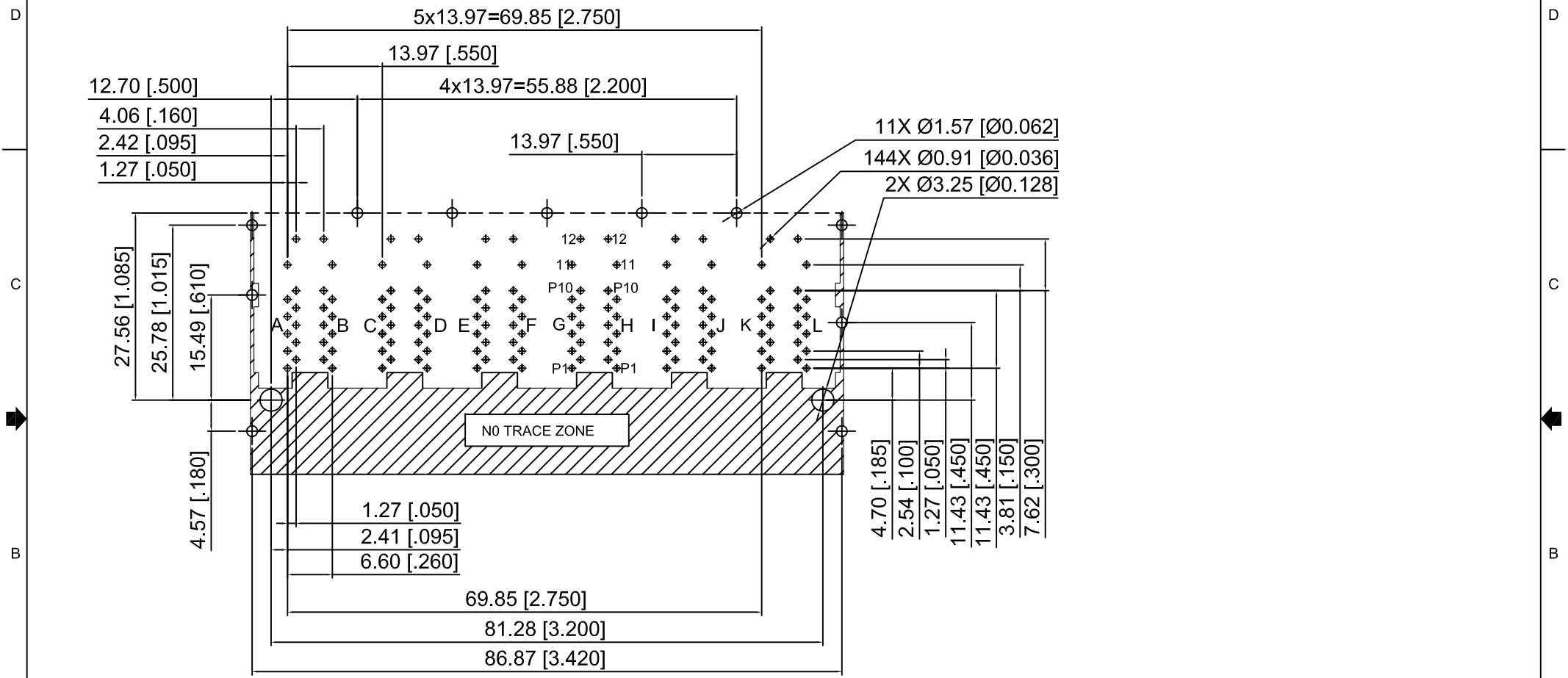
D
C
B
A

D
C
B
A



Description				GLGNET® 深圳方向电子有限公司 SHEN ZHEN GLGNET ELECTRONICS CO.,LTD.			
RJ45 2X6 Through Hole 1000Base-T/PoE+(30W) W/O LED Dimension Tolerance: 0.xx:±0.10[.004] x.xx:±0.25[.010] xx.xx:±0.25[.010]				料号 ITEM NO.		设计 DRAW	WANGKG
				品名 TITLE	JDHA6T908	校对 CHECK	DANNY
				客户 CUSTOMER		核准 APPROVAL	BYANG
比例 SCALE	1:1	单位 UNIT	mm	图号 DWG NO.		日期 DATE	07/25/2019
张数 SHEET	1/3	备注 REMARK		角法 PROJ.		版本 REV	A0

NO.	ECN NO.	DESCRIPTION	REV.	DRAW	DATE



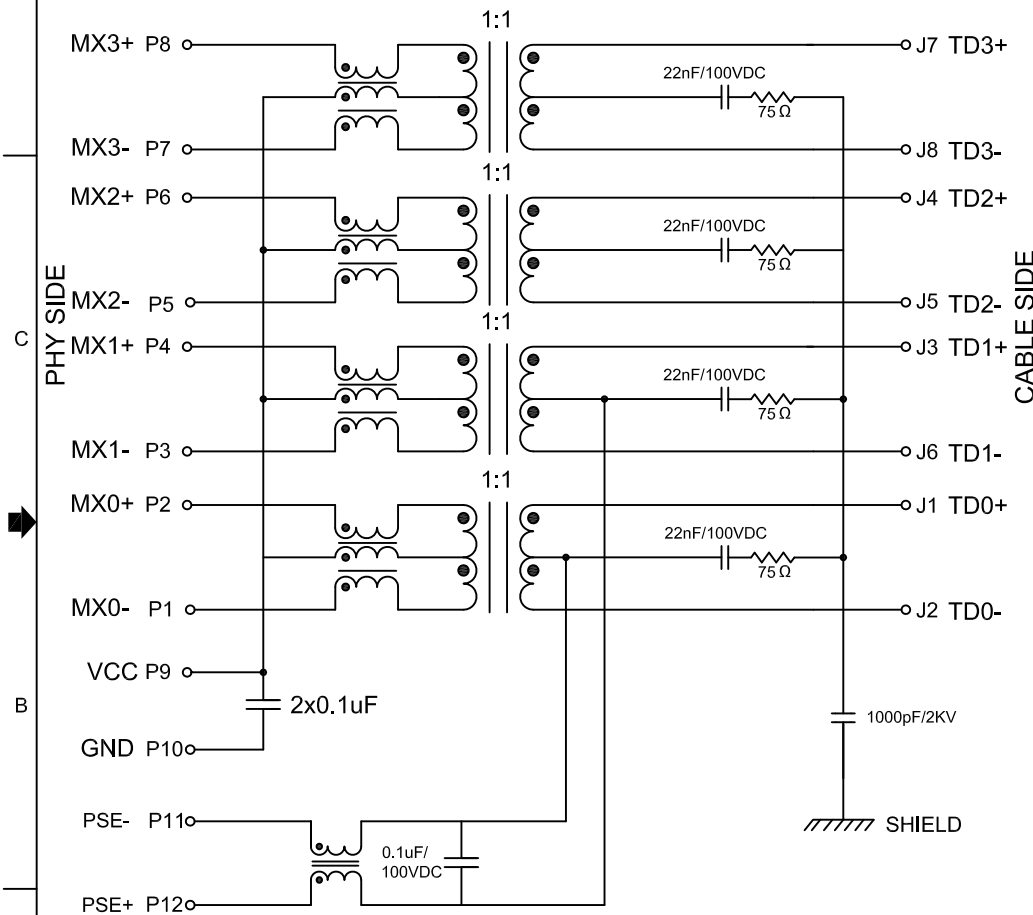
RECOMMENDED P.C.B LAYOUT
TOP VIEW (COMPONENT SIDE)

Dimension Tolerance:
PCB LAYOUT Tolerances:±0.05[0.002]

Description				GLGNET® 深圳方向电子有限公司 SHEN ZHEN GLGNET ELECTRONICS CO.,LTD.			
RJ45 2X6 Through Hole 1000Base-T/PoE+(30W) W/O LED Dimension Tolerance: 0.xx:±0.10[.004] x.xx:±0.25[.010] xx.xx:±0.25[.010]				料号 ITEM NO.		设计 DRAW	WANGKG
				品名 TITLE	JDHA6T908	校对 CHECK	DANNY
				客户 CUSTOMER		核准 APPROVAL	BYANG
比例 SCALE	1:1	单位 UNIT	mm	图号 DWG NO.		日期 DATE	07/25/2019
张数 SHEET	2/3	备注 REMARK		角法 PROJ.		版本 REV	A0

NO.	ECN NO.	DESCRIPTION	REV.	DRAW	DATE

Schematic



PARAMETER	SPECIFICATIONS
OPERATING TEMPERATURE	0°C To +70°C
URNS RATIO	1:1±2%
OPEN CIRCUIT INDUCTANCE(OCL)	220uH MIN@100KHz/100mV With 19mA DC Bias For(CHANNEL1,2) 350uH MIN@100KHz/100mV With 8mA DC Bias For(CHANNEL3,4)
INSERTION LOSS(IL)	-1.2dB MAX@0.3MHz-100MHz;
RETURN LOSS(RL) (Z out=100 OHM)	-18dB MIN@1MHz-30MHz; -13dB MIN@30MHz-60MHz; -10dB MIN@60MHz-100MHz;
CROSSTALK (ADJACENT CHANNELS)	-30dB MIN@1MHz-100MHz;
COMMON MODE REJECTION RATIO(CMRR)	-30dB MIN@1MHz-100MHz;
DC CURRENT/VOLTAGE RATING-PSE PINS	720mA MAXIMUM@57VDC(CONTINUOUS)
HI-POT	2250 VDC@60 SECONDS

Description				GLGNET® 深圳方向电子有限公司 SHEN ZHEN GLGNET ELECTRONICS CO.,LTD.			
RJ45 2X6 Through Hole 1000Base-T/PoE+(30W) W/O LED Dimension Tolerance: 0.xx:±0.10[.004] x.xx:±0.25[.010] xx.xx:±0.25[.010]				料号 ITEM NO.	设计 DRAW	WANGKG	
				品名 TITLE	JDHA6T908	校对 CHECK	DANNY
				客户 CUSTOMER		核准 APPROVAL	BYANG
比例 SCALE	1:1	单位 UNIT	mm	图号 DWG NO.	日期 DATE	07/25/2019	
张数 SHEET	3/3	备注 REMARK		角法 PROJ.	版本 REV	A0	